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1. **An integral equation approach to the periodic steady-state problem in nonlinear circuits**
Frey, D.R.; Norman, O.;
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2. **On an integral equation approach to the steady-state problem in nonlinear circuits**
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3. **A band-pass delta-sigma modulator for ultrasound imaging at 160 MHz clock rate**
Norman, O.;
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4. **A band pass $\Sigma\Delta$ modulator for ultrasound imaging at 160 MHz clock rate**
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5. **An integral equation approach to the periodic steady-state problem in nonlinear circuits**
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1. **On interleaved, differentially encoded convolutional codes**
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3. **Minimum classification error linear regression for acoustic model adaptation of continuous density HMMs**
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4. **On the use of repetition coding with binary digital modulations on mobile channels**
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5. **Coded modulation schemes with turbo and list detection for high-order partial response channels**
Kuznetsov, A.V.;
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6. **Successive-erasure decoding of RS-coded MPSK schemes in a Rayleigh fading channel**
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Hassan, M.A.S.; Sharif, B.S.; Woo, W.L.;
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9. **Optimal detection of M-QAM signal with channel estimation error**
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11. **Iterative decoding of GMSK modulated convolutional code**
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Volume 35, Issue 10, 13 May 1999 Page(s):810 - 811
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Inventor Information for 10/757587

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SITBON, DAVID	RANANA	ISRAEL
NORMAN, ODED	PARDESIA	ISRAEL

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Last Name = ZABARSKI

First Name = BORIS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09606995	6711661	150	06/29/2000	METHOD AND APPARATUS FOR PERFORMING HIERARCHICAL ADDRESS TRANSLATION	ZABARSKI, BORIS
10064336	Not Issued	161	07/02/2002	Communications system using rings architecture	ZABARSKI, BORIS
10064338	Not Issued	161	07/02/2002	Communications system using rings architecture	ZABARSKI, BORIS
10064339	Not Issued	161	07/02/2002	Communications system using rings architecture	ZABARSKI, BORIS
10097598	Not Issued	61	03/15/2002	System and method for longest prefix match for internet protocol lookup	ZABARSKI, BORIS
10307296	Not Issued	61	12/02/2002	Method and system for intertask messaging between multiple processors	ZABARSKI, BORIS
10757587	Not Issued	71	01/15/2004	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	ZABARSKI, BORIS
11288861	Not Issued	30	11/28/2005	Method and system for routing an IP packet	ZABARSKI, BORIS
11695838	Not Issued	25	04/03/2007	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	ZABARSKI, BORIS
60333516	Not Issued	159	11/28/2001	Flexible packet processor for use in communications system	ZABARSKI, BORIS
60440026	Not Issued	159	01/15/2003	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	ZABARSKI, BORIS
60505745	Not Issued	159	09/26/2003	Multi-tier fifo queue	ZABARSKI, BORIS

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
09884377	6891846	150	06/18/2001	METHOD AND APPARATUS FOR A TRAFFIC SHAPER	SITBON, DAVID
10757587	Not Issued	71	01/15/2004	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	SITBON, DAVID
11695838	Not Issued	25	04/03/2007	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	SITBON, DAVID
60440026	Not Issued	159	01/15/2003	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	SITBON, DAVID
60505745	Not Issued	159	09/26/2003	Multi-tier fifo queue	SITBON, DAVID

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08349218</u>	5628026	150	12/05/1994	MULTI-DIMENSIONAL DATA TRANSFER IN A DATA PROCESSING SYSTEM AND METHOD THEREFOR	NORMAN, ODED
<u>08406230</u>	Not Issued	161	03/17/1995	METHOD FOR REDUCING A CACHE MISS PENALTY TIME IN A DATA PROCESSING SYSTEM UTILIZING A READ BEFORE WRITE	NORMAN, ODED
<u>09006212</u>	6076096	150	01/13/1998	BINARY RATE MULTIPLIER	NORMAN, ODED
<u>09061958</u>	6125404	150	04/17/1998	DATA PROCESSING SYSTEM HAVING A PROTOCOL TIMER FOR AUTONOMOUSLY PROVIDING TIME BASED INTERRUPTS	NORMAN, ODED
<u>09263544</u>	6178332	150	03/08/1999	RADIO WITH HALTING APPARATUS AND METHOD	NORMAN, ODED
<u>09263545</u>	6366786	150	03/08/1999	RADIO WITH SYNCHRONIZATION APPARATUS AND METHOD THEREFORE	NORMAN, ODED
<u>09276864</u>	6657977	150	03/26/1999	RADIO WITH BURST EVENT EXECUTION APPARATUS AND METHOD THEREFORE	NORMAN, ODED
<u>09489738</u>	6687813	150	01/21/2000	A DATA PROCESSING SYSTEM AND METHOD FOR IMPLEMENTING ZERO OVERHEAD LOOPS USING A FIRST OR SECOND PREFIX INSTRUCTION FOR INITIATING CONDITIONAL JUMP OPERATIONS	NORMAN, ODED
<u>09499607</u>	Not Issued	161	02/07/2000	IEEE 1149.1 standard compatible integrated circuit having multiple independent testable logic blocks	NORMAN, ODED
<u>10064336</u>	Not Issued	161	07/02/2002	Communications system using rings architecture	NORMAN, ODED
<u>10064337</u>	Not Issued	161	07/02/2002	Communications system using rings architecture	NORMAN, ODED
<u>10064338</u>	Not Issued	161	07/02/2002	Communications system using rings architecture	NORMAN, ODED
<u>10757587</u>	Not Issued	71	01/15/2004	Minimum processor instruction for implementing weighted fair queuing and	NORMAN, ODED

				other priority queuing	
11414240	Not Issued	30	05/01/2006	Device, system and method of accessing a memory	NORMAN, ODED
11695838	Not Issued	25	04/03/2007	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	NORMAN, ODED
60333516	Not Issued	159	11/28/2001	Flexible packet processor for use in communications system	NORMAN, ODED
60440026	Not Issued	159	01/15/2003	Minimum processor instruction for implementing weighted fair queuing and other priority queuing	NORMAN, ODED

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